

ABSTRACT

An auto refresh control circuit of a semiconductor memory device comprises a command decoder for generating 5 an auto refresh signal performing an auto refresh operation; a wordline control means for activating wordlines when the auto refresh signal is activated, and for precharging the wordlines when an auto refresh operation is finished; and a buffer control means for 10 inactivating the input buffers when the auto refresh signal is activated or a power down signal is activated, and for activating the input buffers when a signal detecting the end time of an auto refresh is activated. Accordingly, the auto refresh control circuit inactivates 15 all of the buffers and input latches during the auto refresh operation, thereby reducing current consumption.